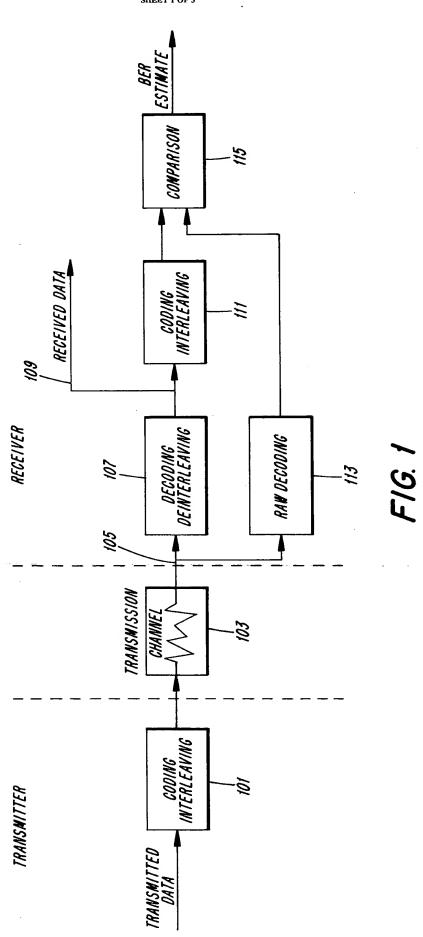
## **AMENDMENTS TO THE DRAWINGS:**

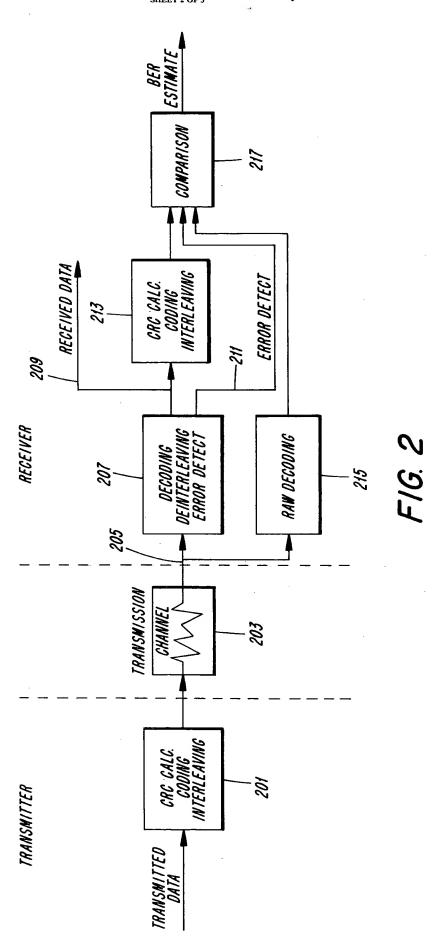
The attached sheets of drawings are formal versions of figures 1-3 submitted with the application filing. These sheets, which include figures 1-3, replaces originals sheets including figures 1-3.





REPLACEMENT SHEET
APPN. FILING DATE: JUNE 21, 2000
TITLE: BIT ERROR RATE ESTIMATION
INVENTOR(S): JOHAN NILLSON
APPLN SERIAL NO.: 09/598,210
SHEET 2 OF 3





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TITLE: BIT ERROR RATE ESTIMATION
INVENTOR(S): JOHAN NILLSON
APPLN SERIAL NO.: 09/598,210
SHEET 3 OF 3

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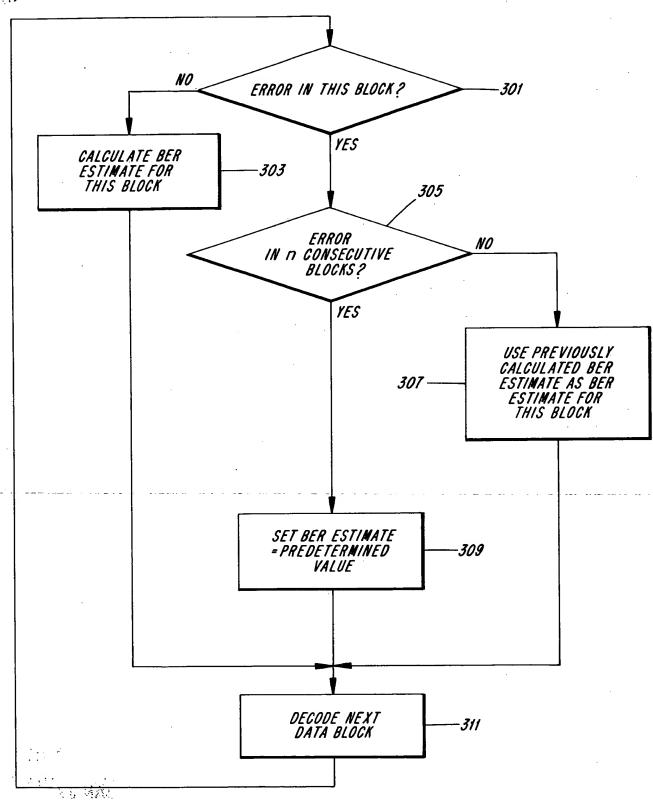


FIG. 3